1

Introduction:

This demo gives details of a complete project, an 8 channel sequential controller which will switch on 8 channels sequentially one by one for a predefined time. For interaction, a LCD module of 2 line x 16 characters and a key pad of 16 keys are provided.

Hardware:

The project hardware contains a LCD 2 line x 16 characters interfaced with the microcontroller using 4 bit data bus. A small key pad with 16 keys is also included for the user interaction. 8 numbers of point LEDs are used to indicate the channel outputs.



Circuit:



Connections:

- 1. Connect port lines P30 to P33 to D4 to D7 line of LCD.
- 2. Connect port lines P37 and P45 to RS and E lines of LCD.
- 3. Connect R/W line of LCD to GND.
- 4. Connect port lines P10 to P13 to Column0 to Column3 of 4X4 matrix keyboard.
- 5. Connect port lines P14 to P17 to Row0 to Row3 of 4X4 matrix keyboard.
- 6. Connect port lines P00 to P07 to eight point LEDs.

Functional Description:

In this module, a complete project for 8 channel sequential controller is studied. The sequential controller has 8 outputs and the user can set individual on time for each channel. Two line by 16 characters LCD is used for displaying the RTC data, 4 X 4 matrix keyboard is used to enter the user setting, Timer X is used generate 1 second delay and port 0 is used for controlling 8 outputs.

Registers Used:

- PD0 Port 0 Direction Register
- PD1 Port 1 Direction Register
- PD3 Port 3 Direction Register
- PD4 Port 4 Direction Register
- PUR0 Pull-up control register 0
- KIEN Key input enable register
- KUPIC Key input Interrupt control register
- TXMR Timer X mode register
- PREX Prescaler X Register
- TX Timer X Register
- TCSS Timer count source setting register



Port Direction Registers:

Bit symbol Bit name Function RW PDi_0 Port Pio direction bit 0 : Input mode RW PDi_1 Port Piz direction bit 1 : Output mode RW PDi_3 Port Pis direction bit 1 : Output mode RW PDi_5 Port Pis direction bit 1 : Output mode RW PDi_5 Port Pis direction bit RW PDi_6 Port Pis direction bit RW PDi_7 Port Pir direction bit RW	ь7	b6 b5	54	ЬЗ	h2	61	ы	Symbol PD0 PD1 PD3 PD4	Addres 00E21 00E31 00E71 00EA1	ss After rese 16 0016 6 0016 16 0016 16 0016	ət
PDi_0 Port Pio direction bit RW PDi_1 Port Pit direction bit Functions as an input port) PDi_2 Port Piz direction bit Functions as an output port) PDi_3 Port Piz direction bit Functions as an output port) PDi_4 Port Pis direction bit Functions as an output port) PDi_5 Port Pis direction bit RW PDi_6 Port Pis direction bit RW PDi_7 Port Pir direction bit RW				1			1	Bit symbol	Bit name	Function	RW
PDi_1 Port Pit direction bit 0: Input mode RW PDi_2 Port Pi2 direction bit 1: Output mode RW PDi_3 Port Pi3 direction bit 1: Output mode RW PDi_4 Port Pi3 direction bit 1: Output mode RW PDi_5 Port Pi6 direction bit RW PDi_6 Port Pi6 direction bit RW PDi_7 Port Pi7 direction bit RW	i.	11	i	i	İ.	i	i.	PDi_0	Port Pi0 direction bit		RW
PDi_2 Port Pi2 direction bit 1 : Output mode RW PDi_3 Port Pi3 direction bit 1 : Output mode RW PDi_4 Port Pi4 direction bit RW PDi_5 Port Pi6 direction bit RW PDi_6 Port Pi6 direction bit RW PDi_7 Port Pi7 direction bit RW						Ŀ.,		PDi_1	Port Pit direction bit	0 : Input mode (Eupctions as an input port)	RW
PDi_3 Port Pi3 direction bit (Functions as an output port) RW PDi_4 Port Pi4 direction bit RW PDi_5 Port Pi6 direction bit RW PDi_6 Port Pi6 direction bit RW PDi_7 Port Pi7 direction bit RW					I			PDi_2	Port Pi2 direction bit	1 : Output mode	RW
PDi_4 Port Pi4 direction bit RW PDi_5 Port Pi5 direction bit RW PDi_6 Port Pi6 direction bit RW PDi_7 Port Pi7 direction bit RW		11	1	÷				PDi_3	Port Pi3 direction bit	(Functions as an output port)	RW
PDi_5 Port Pi5 direction bit RW PDi_6 Port Pi6 direction bit RW PDi_7 Port Pi7 direction bit RW	1	!!	١.,					PDi_4	Port Pi4 direction bit		RW
PDi_6 Port Pi6 direction bit RW PDi_7 Port Pi7 direction bit RW		L						PDi_5	Port Pi5 direction bit		RW
PDi_7 Port Pi7 direction bit RW	i						PDi_6	Port Pi6 direction bit]	RW	
	i				PDi_7	Port Pi7 direction bit		RW			

Notes:

The PD0 register must be written to by the next instruction after setting the PRC2 bit in the PRCR register to "1" (write enabled).

2. Nothing is assigned to the PD3_4 to PD3_6 bits in the PD3 register. When writing to the PD3_4 to PD3_6 bits, write "0" (input mode). When read, its content is indeterminate.

3. Nothing is assigned to the PD4 0 to PD4 4. PD4 6 and PD4 7 bits in the PD4 register. When writing to the PD4_0 to PD4_4, PD4_6 and PD4_7 bits, write "0" (input mode). When read, its content is indeterminate.

PD0 is set to H'FF to select the port lines P00 to P07 as output lines to control the LED outputs. PD1 is loaded with H'0F to select port lines P14 to P17 as output lines to connect matrix keyboard. The port line P45 is selected as output line by setting bit PD45 in PD4 register. The port lines P30 to P33 and P37 are selected as output lines by moving H'8F to PD3 register.

PUR0 - Pull-up Control Register:

ьт		Symbol PUR0	Address 00FC16	After reset 00XX00002	
1		Bit symbol	Bit name	Function	RW
i.	4	PU00	P00 to P03 pull-up1	0 : Not pulled up	RW
		PU01	P04 to P07 pull-up1	1 : Pulled up ¹	RW
		PU02	P10 to P13 pull-up1		RW
1		PU03	P14 to P17 pull-up1		RW
	l	(b5-b4)	Nothing is assigned. When write, set to "0". Whe	n read, its content is indeterminate.	Ι
1	i	PU06	P30 to P33 pull-up1	0 : Not pulled up	RW
i		PU07	P37 pull-up1	1 : Pulled up ¹	RW

Notes: 1. The pin for which this bit is "1" (pulled up) and the direction bit is "0" (input mode) is pulled up.

The bit PU02 is set to 1 to enable internal pull ups for port lines P10 to P13.



b7 b6	b6 b4 b3	5 b2 b1 b0	Symb KIEM	ol Address N 009810	After reset 0016	
			Bit symbol	Bit name	Function	RW
			KIOEN	KI0 input enable bit	0 : Disabled 1 : Enabled	RW
		ļ	KIOPL	KI0 input polarity select bit	0 : Falling edge 1 : Rising edges	RW
		l	KI1EN	KI1 input enable bit	0 : Disabled 1 : Enabled	RW
			KI1PL	KI1 input polarity select bit	0 : Falling edge 1 : Rising edges	RW
	ļ L		KI2EN	KI2 input enable bit	0 : Disabled 1 : Enabled	RW
	L		KI2PL	KI2 input polarity select bit	0 : Falling edge 1 : Rising edges	RW
l.			KI3EN	KI3 input enable bit	0 : Disabled 1 : Enabled	RW
L			KI3PL	KI3 input polarity select bit	0 : Falling edge 1 : Rising edges	RW

KEIN - Key Input Enable Register:

 The IR bit in the KUPIC register may be set to "1" (interrupt requested) when the KIEN register is rewritten. Refer to the paragraph 19.2.5 "Changing Interrupt Factor" in the Usage Notes Reference Book.

The register KIEN is loaded with H'55 to enable KI0 to KI3 key input interrupts and to select falling edge detection.

KUPIC - Key Input Interrupt Control Register:

	INT3IC TCIC CMP0IC	005A 005B 005C	16 XXXXX0002 16 XXXXX0002 16 XXXXX0002	
	Bit symbol	Bit name	Function	RW
	ILVLO	Interrupt priority level select bit	525150 000: Level0(interrupt disabled) 001: Level1	RW
	ILVL1		010:Level2 011:Level3 100:Level4 101:Level5	RW
L	ILVL2		110: Level 6 111: Level 7	RW
	IR	Interrupt request bit	0 : Interrupt not requested 1 : Interrupt requested	RW
	(b7-b4)	Nothing is assigned. When write, set to "0". Wh	en read, its content is indeterminate.	_

The priority level 2 is selected for key input interrupt by moving H'02 to KUPIC register.



TXMR - Timer X Mode Register:

67	bß	b5	b4	63	b.	2	61	60	Symbol TXMR	Addres 008B1	ss Afterreset 6 0016	
T	Τ	T						Τ.			-	- 4.44
	1	1							Bit symbol	Bit name	Function	Parr
								L	TXMOD0	Operation mode select bit 0, 1	0 0 : Timer mode or pulse period measurement mode	RW
							ί.		TXMOD1		1 0 : Event counter mode 1 1 : Pulse width measurement mode	RW
		ļ			ļ				R0EDG	INT1/CNTR0 polarity switching bit ¹	Function varies with each operation mode	RW
	l			L					TXS	Timer X count start flag	0 : Stops counting 1 : Starts counting	RW
	l	ł	Ļ						TXOCNT	P30/CNTRo select bit	Function varies depending on operation mode	RW
l	l	L							TXMOD2	Operation mode select bit 2	0 : Except in pulse period measurement mode 1 : Pulse period measurement mode	RW
	L.								TXEDG	Active edge reception flag	Function varies depending on operation mode.	RW
Ľ									TXUND	Timer X under flow flag	Function varies depending on operation mode.	RW

 The IR bit in the INT1IC register may be set to "1" (interrupt requested) when the R0EDG bit is rewritten. Refer to the paragraph 19.2.5 "Changing Interrupt Factor" in the Usage Notes Reference Book.

Timer X register TXMR is loaded with H'00 to set timer mode.

PREX - Prescaler X Register:

b7	ьо	Symbol Adı PREX 00	dress 8C16	After reset FF16	
	Mode	Function		Setting range	RW
L	Timer mode	Internal count source is c	counted	0016 to FF16	RW
	Pulse output mode	Internal count source is o	ounted	0016 to FF16	RW
	Event counter mode	Externally input pulses an	re counted	0016 to FF16	RW
	Pulse width measurement mode	Pulse width of externally pulses is measured (Internal count source is	input counted)	0016 to FF16	RW
	Pulse period measurement mode	Pulse period of externally pulses is measured (Internal count source is)	/input counted)	0016 to FF16	RW

The prescaler register PREX is loaded with 100 to divide the selected input clock to timer X by 100.



TX - Timer X Register:



The timer X register, TX, is loaded with 100 to generate an interrupt at 1 milli second rate.

TCSS - Timer Count Source Setting Register:

b7 b	6) D	b5	b4	ЫЗ	52	p.	1	50	Symbol TCSS	Address 008E16	After reset 0016	
									Bit symbol	Bit name Timer X count source	Function	RW
						ļ			TXCK1	select bit1	00:f1 01:f8 10:f82 11:f2	RW
					L				TYCK0	Timer Y count source select bit ¹	63 62 0 0 : f1 0 1 : f8	RM
				i	 				TYCK1		10 : fring 11 : Selects input from CNTR+ pin	RM
			I.		 				TZCKO	Timer Z count source select bit ¹	bisbet 00:ft 01:fa	RW
		١.			 				TZCK1		1 0 : Selects Timer Y underflow 1 1 : f2	RW
L	.				 				(b7-b6)	Reserved bit	Must be set to "0"	RW

Notes: 1. Avoid switching a count source, while a counter is in progress. Timer counter must be stopped before switching a count source.

The data H'03 is moved to Timer Count Source Setting Register TCSS to select "f2" as source clock for Timer X.



Software Description:

After reset, the following initialization are taking place before entering into the main loop.

- 1. Initialize the port 0 as output to control outputs.
- 2. Initialize the port lines P14 to P17 as output lines and enable KI0 to KI3 key input interrupts.
- 3. Initialize port lines P30 to P33, P37 and P45 as output lines and initialize the LCD in 4 bit mode.
- 4. A message "sequential Contr" is displayed on the 1st line of LCD.
- 5. Initialize Timer X to generate an interrupt at a rate of 1 KHZ.

Key input interrupt is generated for any key press in the keyboard. In the Key Input (KI0 to KI3) interrupt service routine, the pressed key value is identified.

In the main loop, the keyboard status is checked and the control goes to setting function if the "SET" key was pressed. Otherwise the program will decrement the currently selected channel's on time for every second and activates the output line meant for that channel till the on time becomes zero. The currently selected channel number gets incremented and the loop is repeated until the last channel. After this, the control starts repeating the whole thing again.



Files	Description
Demo7.C	Main file for this module, will sequentially switches on 8 channels one after another for the programmed on time and checks the keyboard for user interaction.
R8C1113_FE_LCD_4Bit.C	LCD routines to initialize LCD, cursor on/off, display a message etc.
R8C1113_FE_LCD_4Bit.H	Declarations of functions in R8C1113_FE_LCD_4Bit.C
R8C1113_FE_Keyboard.C	Keybaord routines to initialize Key input interrupts, I/O lines used by keyboard and key input interrupt service routine.
R8C1113_FE_Keyboard.H	Declarations of functions in R8C1113_FE_Keyboard.C

The files used in this module are listed below:



Functions	Description
main	This is main function of sequential controller and will switch 8 channels one after another on with the set time. Checks the keyboard for "SET" key press. Input: None. Output : None.
InitializeSequentialController	Initialize I/O lines, Keyboard and LCD. Input: None. Output : None.
ProcessTimer_X_Int	Timer X interrupt service routine and will count the milli seconds and set a flag after 1000 milli seconds. Input: None. Output : None.
InitializeTimerX	Initializes Timer X to generate an interrupt for every one milli second. Input: None. Output : None.
DisplayChannelNumberOntime	Displays Channel number and on time of the currently selected channel. Input : None. Output : None.
ConvertBCD	Convert two digit hex number into BCD number. Input: None. Output : None.
DisplayLCD2DigitBCD	Displays the given 2 digit hex number in BCD format on LCD at given location. Input: Line number, character position and data. Output : None.
BCDDecrement	Decrements the given 2 digit number by one in BCD format. Input: 2 digit BCD data. Output : Decremented 2 digit BCD data.

The functions in the file "Demo7.C" and short descriptions are listed below:



The functions in the file "R8C1113_FE_LCD_4Bit.C" and short descriptions are listed below:

Functions	Description
DisplayLCD	Displays a message (16 Characters) on LCD on the given line number. Input: Line number and message string. Output : None.
DisplayLCD2Digit	Displays the given 2 digit number on LCD at given location. Input: Line number, character position and data. Output : None.
CursorON	Makes the cursor visible on LCD. Input: None. Output : None.
CursorOFF	Hides the cursor. Input: None. Output : None.
InitializeLCD	Initializes the I/O lines used by LCD and LCD in 4 bit mode. Clears the LCD. Input: None. Output : None.
WriteDataLCD	Write a data byte to LCD. Input: Data Byte. Output : None.
WriteCommandLCD	Write a command byte to LCD. Input: Command Byte. Output : None.



The functions in the file "R8C1113_FE_Keyboard.c" and short descriptions are listed below:

Functions	Description
ReadKeyboardStatus	Returns the keyboard Input: None. Output : Keyboard Status.
WaitForKeyPress	Waits for a key press and returns the key code of the pressed key. Input: None. Output : Key Code.
ReadKeyCode	Reads and returns the last pressed key code without waiting for a key press. Input: None. Output : Key Code.
InitializeKeyboard	Initializes the I/O lines used by keyboard and enables Key input (Kl0 to Kl3) interrupts. Input: None. Output : None.
ProcessKey_Int	Interrupt service routine for key input interrupt. Input: None. Output : None.



Program Flow:

For Main Loop







For Setting Routine



Execute Demo:

After reset, the program will display a message "**Sequential Contr**" on the first line of LCD and the channel number and on time in second line as shown below:

"Sequential Contr Channel x: yy "

Where x is the current channel number and yy is the on time of the current channel.

The program switches the outputs on one after other for the set time.



Use Topview Simulator to Verify the Design.

Open the project Demo7 in the R8C/Tiny System Simulator using **Open Project** option from **Project menu**. The project window opens up along with the Demo7.c file. Use **Build** option from **Build** menu to compile the project. An output window captures the compiler ouput.

Use **Project** -> **Download Project** from main menu to download the .mot file into the simulator's memory for simulation.

SANGO - R8C/Tiny System Simulator		
Eile Project Memory View Run Build Break	akPoint SingleStep <u>W</u> indow <u>H</u> elp	
	*	
🖻 Project 📃 🗖 🗙	Demo7.c	
E- To Demo7	1 /************************************	<u>^</u>
E C Source Files	2 /* 8 Channel Sequential Controller with individual ontime setting. */ 3 /* */	
Demo7.c Evector c	4 /* 2 Lines by 16 characters LCD for displaying timing. */	
Heap.c	5 /* 4 by 4 marix keyboard for entering the time setting. */	
Initsct.c	7 /* Eight point LEDS are used to indicate the output status. */	
	8 /* The program sequentially switches one channel after another upto */	
Resetprg.c	9 /* 8 channels. After 8th channel it goes to first channel. */	=
R8C1113_FE_A21.c	10 /* Each channel will stay in on state for the programmed ontime. */	
R8C1113_FE_Keyboard.c	12 /* LCD Connection:- */	
	13 /* */	
	14 /* R3 - P37 */ 15 /* BH - CND */	
	16 /* E - P45 */	
	17 /* D4 - P30 */	
	18 /* D5 - P31 */	
	20 /* D7 - P33 */	
	21 /* */	
	22 /* Keyboard Connection:- Keyboard Layout:- */ 23 /* */	
	24 /* RowO - P14 [Esc] [Set] [] [] */	
	25 /* Row1 - P15 [8] [9] [Next] [Back] */	
	26 /* Row2 - P16 [4] [5] [6] [7] */	
	28 /* ColumnO - P10 (KIO) */	
	29 /* Column1 - P11 (KI1) */	
	30 /* Column2 - P12 (KI2) */	
	32 /* */	
	33 /* Output:- */	
	34 /* */	
	36 /* Channel 2 - P01 */	~
		.::
Output		
CODE 0001710(006AEH) Byte(s The value of option function se	s) slect register is FFH	<u>~</u>
<< Linking Completed >>		
Converting to S Record(MOT) For Load Module Converter (1mc30) f Copyright(C) 2005. Renesas Tech and Renesas Solutions Corp., Al	rmat for RGC/Tiny,M16C/60 Series Version 4.01.01.000 hnology Corp. 11 Rights Reserved.	
-		
<< Convertion Completed >>		
<< Building Completed >>		
<		
Ready	R5F21134 No Breakpoint Set M/C:4709113	3 On-Chip Low Speed/8

Do the settings to the LCD modules as shown. Connect LCD control and data lines to port lines P37, P33 to P30 and P45.





Make the setting for keyboard module as shown below:

Keyboard Module Setting		
Keys - Momentary type Select Port Lines 7 6 5 4 3 2 1 0 Port 0 Port 1 Port 3 Port 3 Port 4 Click the checkbox to Connect a Momentary Switch	Activating Levels 7 6 5 4 3 2 1 0 Pot 0 Pot 1 Pot 3 Pot 4 Click the checkbox to activate the Switch at '1' level	Keys - Toggle Type Select Port Lines 7 6 5 4 3 2 1 0 Port 0 Port 0 Port 1 Port 3 Port 4 Cick the checkbox to Connect a Toggle Switch
Matrix KeyPad Keypad Type C 4×3 C 4×4 C 4×4 C 4×8 C None Port Line Selection Row2 Row3 Row4 Column1 Column3 Column3 Column4	Port lines 7 6 5 4 3 2 1 0 Port 0 0 0 0 0 0 0 0 0 Port 1 0 0 0 0 0 0 0 Port 3 0 0 0 0 0 Port 4 0 0 0	Summay: Row1 - P1_4 Row2 - P1_5 Row3 - P1_6 Row4 - P1_7 Column1 - P1_0 Column2 - P1_1 Column3 - P1_2 Column4 - P1_3
Key Name Setting Disconnect all Port lines		<
OK		Cancel



ED Select Port Lines	Seven Segment Display	
76543210 Devo R R R R R R R R	Interface Selection Display Color Da	ta Input Display Type
	C Non-Multiplexed C Red G	7 Seament Common Anode
Port 3 F FFF	© Multiplexed C Green C	BCD C Common Cathode
Check the checkbox to Connect an LED	Multiplexer Options	tivating Level
A.C. C. I. I.	© Internal © 2 to 4	F 0
Activating Levels	C External C 3to 8	~ 1
	C 4 to 16	
Port 4		
Check the checkbox to activate	Port Line Selection	Summary:
the LED at level 1	- Port lines	Segement a - PO_ Segement b - PO_
LED Color	7654	3 2 1 0 Segment c - P0 Segment d - P0
Red C Green	Port1CCCC	CCCC Segement e - PO_
	Port 3 C Port 4 C C C	Segment g - P0_ Segment dp - P0_
	Disconnect all Port	T I I avail
OK Cancel	lines U Level 4	I LEVEI

Connect 8 numbers of point LEDs to the port lines P00 to P07 using LED module setting.

Then open the LED, LCD, Keyboard windows and arranage them as shown below.



SANGO - RBC/Tiny System Simulator					- 7 🗙
Eile Project Memory View Run Build BreakPoint SingleStep Window Help					
	PLI Go orre St	EL STEP EL 🗮	? STOP #		
LCD - 2 Lines X 16 Characters	Program				
	Address BH	• Opcodes	Mnemonic	S	
<mark>bequential Lontr</mark>	0C05F	FE F7	JMP.B	0C057H	
	0C061	F3	RTS		
<u>unannei i : 08</u>	0C062	7E 9F 50 00	BSET	0,000AH	
LED and 7 Segment Displays	0C066	C7 08 06 00	MOV.B	#08H,CM0	
	0C06A	C7 68 07 00	MOV.B	#68H, CM1	
	0C06E	7E 8F 62 00	BCLR	2,000CH	
P0 0 P0 1 P0 2 P0 3 P0 4 P0 5 P0 6 P0 7	0C072	7E 9F 3C 00	BSET	4,0007H	
	0C076	7E 8F 50 00	BCLR	0,000AH	
	0C07A	F3	RTS		
	0C07B	20	ADD . B	ROH, ROL	
	0C07C	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
	0C07E	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
	0C080	FB	REIT		
	0C081	04	NOP		
	0C082	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
×	0C084	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
< >,;;	0C086	FB	REIT		
🗒 Matrix Keyboard and Keys 📃 🗖 🔀	0C087	04	NOP		
	0C088	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
Matrix Keypad 4 X 4	0C08A	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
0 1 2 3	0C08C	FB	REIT		
	0C08D	04	NOP		
4 5 6 7	0C08E	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
	0C090	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
8 9 Next Back	0C092	FB	REIT		
	0C093	FD EC FD ED	JSR.A	DFDECH	
Foc Sat	0C097	BF FB 04	NOT . B	04FBH	
	0C09A	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
	0C09C	FD 80 C3 00	JSR.A	0C380H	
	0C0A0	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
	OCOA2	FB	REIT		
	0C0A3	04	NOP		
	0C0A4	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
	0C0A6	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
	0C048	FB	REIT		
	0C0A9	04	NOP		
	OCOAA	EC FD	PUSHM	R0,R1,R2,R3,A0,A1,FB	
	OCOAC	ED BF	POPM	R0,R1,R2,R3,A0,A1,FB	
	OCOAE	FB	REIT		
	OCOAF	04	NOP		~
Executing from 0C6CCH		R5F21134	No Breakpoint Set	M/C:8101683	External Clock/2

Down load the program using **Download Project** command in **Project** menu.

Run the program using **Go** command in **Run** menu.

The program sequentially switches one channel after another upto 8 channels. After 8th channel it goes to first channel. Each channel will stay in on state for the programmed ontime. The channel number and ontime are displayed in LCD as shown below:

"Sequential Contr Channel 1: 07 "

